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1	Chauhan Chand Hitendrasinh, Dr. Vijay Makwana	THYRISTOR CONTROLLED SERIES CAPACITOR: SIMULATION AND ANALYSIS	International Journal of Advance Engineering and Research Development	2348-4470
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3	Nita Patil, J. H. Patil	FPGA Implementation of Direct Digital Frequency Synthesizer With CORDIC Algorithm	Global Journal for Research Analysis	2277-8160
4	Nilay Desai,Utpal Pandya, Chintan Desai	Active Noise Control Using LMS & NLMS Algorithm	Journal of Engineering and Technology	2348-4071
5	Ankit Adesara, Rakesh Gajre, Rashmi Verma	Design of 1-Bit SRAM Cell Using FinFET	International Journal of Innovative Research in Electrical, Electronicsa, Instrumentation and Control Engineering	2321-2004
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