APPLICATION FORM

One Day Workshop on "High Voltage Engineering" 27th January, 2020

Name and Address of the applicant:

College:	
Gender: M/F DOB:	
Qualification:	
Experience:	
Designation:	
Mobile:	
Email:	
Accommodation required	d? Yes/No:
Address of Sponsoring Au	

PAYMENT DETAILS (In CASH only):

Signature of the Applicant

Date:

Rs.

The applicant will be permitted to participate in the above program if selected.

WORKSHOP PROGRAM THEMES

• High Voltage Engineering

PRACTICAL SESSIONS ON

- Virtual laboratory of high voltage
- 150 kV Impulse Generator
- Capacitance Tan ð Measurement
- Liquid ,solid Di-electric breakdown
- Electrolytic Tank
- Horn Gap Apparatus
- AC/DC test on different Types of Gaps
- Transformers oil test

WHO CAN APPLY?

- Faculty members and industrial person.
- Research Scholars.
- Engineering students.

SPEAKER

Mr. Jay Patel Assistant Professor, EE Department C.G.Patel Institute of Technology, Bardoli

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COURSE FEE

Students/ Research scholars Rs. 100/-

Institute/ College Faculties Rs. 200/-

One Day Workshop

ON

High Voltage Engineering 27th January, 2020

Sponsored by

Uka Tarsadia University, Maliba Campus, Gopal Vidhyanagar, Bardoli, Surat





Accredited with B+ 2.74 Grade by NAAC, Govt. of India



Power & Energy Society® Gujarat Chapter

Organized By

Department of Electrical Engineering C. G. Patel Institute of Technology Uka Tarsadia University Bardoli-Mahuva Road, Dist-Tarsadi Surat-394350



Signature of Head of the Institution with Seal

ABOUT THE UNIVERSITY

Bardoli Pradesh Kelavani Mandal (BPKM) is a public trust established in 1960 to cater to the educational needs of the tribal belt of South Gujarat region. Academic programs across the disciplines of Management, Computer Science, Engineering and Technology, Bio Technology, Micro Biology, Science, Pharmacy, Commerce, Nursing, Physiotherapy, Architecture and institute of Sports were established in the campus. In 2008, the Maliba campus became full-fledged Uka Tarsadia University.

ABOUT THE INSTITUTE

UTU has started a self-financed engineering institute named as Chhotubhai Gopalbhai Patel Institute of Technology (CGPIT) in 2009 with the view to educate and train future professionals to lead in today's competitive environment. CGPIT_ offers Diploma, B. Tech and M. Tech and Ph.D. programs in various disciplines.

ABOUT THE DEPARTMENT

The department is one of the pioneering departments of the Institute. The department has highly qualified faculty members engaged in teaching and research with the aim of achieving excellence in the field of Electrical Engineering. The department offers Under Graduate course in Electrical Engineering and Post Graduate programme in Power Systems.

ABOUT THE PROGRAM

In modern times, high voltage is used for a wide variety of applications covering power systems, industry and research laboratories. The workshop intends to cover both conventional and emerging test and measuring techniques. It is needless to

emphasize the importance of evaluation of test results, and the recent advances therein. The workshop is intended to facilitate the participants in acquiring knowledge and information on generation of high voltages, latest trends in high voltage measurements, latest standards/test procedures and various methods of evaluation of test results to diagnose the equipment for failure detection. The participants are also intended to be exposed to practical demonstration on high voltage testing and measurement techniques on an electrical equipment in the laboratory which will benefit them in understanding the high voltage test and measurement techniques. This workshop is mostly organised for final year Bachelor students, Master students, Researchers and Academicians who are taking keen interest in High Voltage Engineering

CHIEF PATRON

Dr. Dinesh R. shah, Provost, UTU

PATRON:

Dr. R .V Patil, Director, CGPIT

CONVENER:

Mr. Ankur Rana, Head of EE Department, CGPIT

ORGANIZING SECRETARY:

Mr. Jay Patel Assistant Professor , EE Department ,CGPIT

PROGRAMME COORDINATORS

Mr. Hiren Sidhpuria Mr. Darshan Vora Mr. Lokesh Shah Mr. Arjun Jariwala Mr. Bhargav Patel

Ms. Arpita Patel Mr. Ranjit Rajat Ms. Nidhi Shah Ms. Hinal Surati Ms. Unnati Mali

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REGISTRATION & GENERAL INFORMATION

Applications for the participation in the workshop should reach in the attached format via regular mail at the following address:

Mr . Jay Patel

Organizing Secretary Electrical Engineering Dept. C. G. Patel Institute of Technology, UTU, Bardoli, Surat, Gujarat, India, 394350.

The last date of reaching applications is **23rd January, 2020**. The candidates will be informed of their selection through E-mail by **24th January, 2020** on the basis of first come first serve.

The breakfast and Lunch will be provided to the participants. Limited seats are available in this course. The participants would not be paid TA and DA.

ACCOMMODATION

Suitably furnished accommodation will be made available, if requested in advance, in the hostels/guest houses of the CGPIT on nominal payment basis for out stationed candidates on twin sharing basis.

ADDRESS FOR COMMUNICATIONS: Mr. Jay Patel Mr. Hiren Sidhpuria

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