



छोटुभाई गोपालभाई पटेल प्रौद्योगिकी संस्थान, बारडोली  
Chhotubhai Gopalbhai Patel Institute of Technology, Bardoli

**CHHOTUBHAI GOPALBHAI PATEL  
INSTITUTE OF TECHNOLOGY, UTU  
ELECTRONICS & COMMUNICATION  
ENGINEERING DEPARTMENT /  
INFORMATION AND  
COMMUNICATION TECHNOLOGY  
DEPARTMENT**

**Report on One day hands on workshop on  
“VLSI Design Technology”  
2024-25**

**Date: 23/09/2024**

**Venue: C-201 & Computer Lab – 1 and 2, Department of EC and ICT, CGPIT**

**Time: 09:00 AM – 03:30 PM**

**Total No. of Participants: 23**

**Name of the Keynote Speaker: Prof. Chintan Panchal  
(Assistant Professor, SCET, Surat)**

**Event Coordinators: Mr. Chintan Desai, Dr. Mayank Kapadia**

**Program objective:** To implement the VLSI design module and apply it in the core engineering field and products.

**Program outline:** The one-day hands on workshop were arranged in such a way that students can understand and implement the VLSI schematic and layout design and apply the same in real life.

**Program outcomes:** Participants understand the fundamental concepts of VLSI design technology, performs hands on practice and trouble shoot the problems while using a software model and carry out the project work using the same.

**Schedule of Events:**

<b>Time</b>	<b>Activity</b>
09:00 AM – 09:30 AM	Tea and Breakfast
09:30 AM – 09:45 AM	Inauguration of Workshop, Welcome of Keynote Speaker and Participants
09:45 AM – 12:30 PM	Hands on Session-1
12:30 PM – 01:15 PM	Lunch Break
01:15 PM – 03:15 PM	Hands on Session-2
03:15 PM – 03:30 PM	Valedictory, Thanks Giving to Keynote Speaker and Distribution of Certificates

**List of Participants:**

<b>Name of Participant</b>	<b>Stream</b>	<b>Name of Program</b>
Chirag Patil	Engineering	B.Tech ICT Engineering 7th Sem
Sutaria Mann Manishbhai	Engineering	B.Tech ICT Engineering 7th Sem
Dayani Tisha Rajubhai	Engineering	B.Tech ICT Engineering 7th Sem
Sutariya Riddhi Dineshbhai	Engineering	B.Tech ICT Engineering 7th Sem
Jariwala Trisha	Engineering	B.Tech ICT Engineering 7th Sem
Ajith Bhasharan	Engineering	B.Tech ICT Engineering 7th Sem
Bhavini Ajaysinh Dodiya	Engineering	B.Tech ICT Engineering 7th Sem
Suthar Manishkumar Sanwalaram	Engineering	B.Tech ICT Engineering 7th Sem

Rathod Gautam Manoj	Engineering	B.Tech ICT Engineering 7th Sem
Savita Yadav	Engineering	B.Tech ICT Engineering 7th Sem
Ahir Nitin Vallabhbbhai	Engineering	B.Tech ICT Engineering 7th Sem
Patel Harsh Kiritbhai	Engineering	B.Tech ICT Engineering 7th Sem
Deviprasad Swain	Engineering	B.Tech ICT Engineering 7th Sem
Sameer Jahangir Alam	Engineering	B.Tech ICT Engineering 7th Sem
Vaibhavi Chandramani Dwivedi	Engineering	B.Tech ICT Engineering 7th Sem
Kasak Vijay Patel	Engineering	B.Tech ICT Engineering 5th Sem
Patel Bhumikkumar Rajesh	Engineering	B.Tech ICT Engineering 3rd Sem
Jaykumar Roshanlal Sharma	Engineering	B.Tech EC Engineering 7th Sem
Aayush Kumar Jha	Engineering	B.Tech EC Engineering 7th Sem
Shweksha Premshankar Singh	Engineering	B.Tech EC Engineering 7th Sem
Patel Hetkumar Dharmeshbhai	Engineering	B.Tech EC Engineering 3rd Sem
Chirag Vinod Patel	Engineering	B.Tech EC Engineering 3rd Sem
Vasoya Mahek Ishvarbhai	Engineering	B.Tech EC Engineering 3rd Sem

### **Details of the One Day Hands on Workshop:**

The main objective of the one day hands on workshop on “VLSI Design Technology” is to make EC and ICT Engineering students familiar with the latest and advanced software tools available for simulation and design implementation, to provide students a more practical approach, practical implementation of theoretical concepts and fundamentals into the field of VLSI design work, and also to make them prepare for industrial requirements and industry based projects.

Prof. Chintan Panchal, Assistant Professor, SCET, Surat was invited as a keynote speaker to conduct the one day hands on workshop on “VLSI Design Technology” of EC and ICT engineering. He delivered the basics and pre-requisite for the hands-on workshop for one hour before beginning of hands-on workshop. All students took active participation with great enthusiasm to gain the knowledge on VLSI design. All the participants were highly benefited by the vast experience of Prof. Chintan Panchal in the field of VLSI.

Prof. B. M. Vadher – Director, CGPIT, Dr. Mayank Kapadia – Head of the Department, EC and ICT Engineering, Keynote speaker Prof. Chintan Panchal, Prof. Chintan Desai – Coordinator of one day hands on workshop, Prof. Nita Patil , Prof. Nupur Pathak encouraged students and provided their valuable guidance and suggestions to all the students to participate in such workshop and training program in future as well.

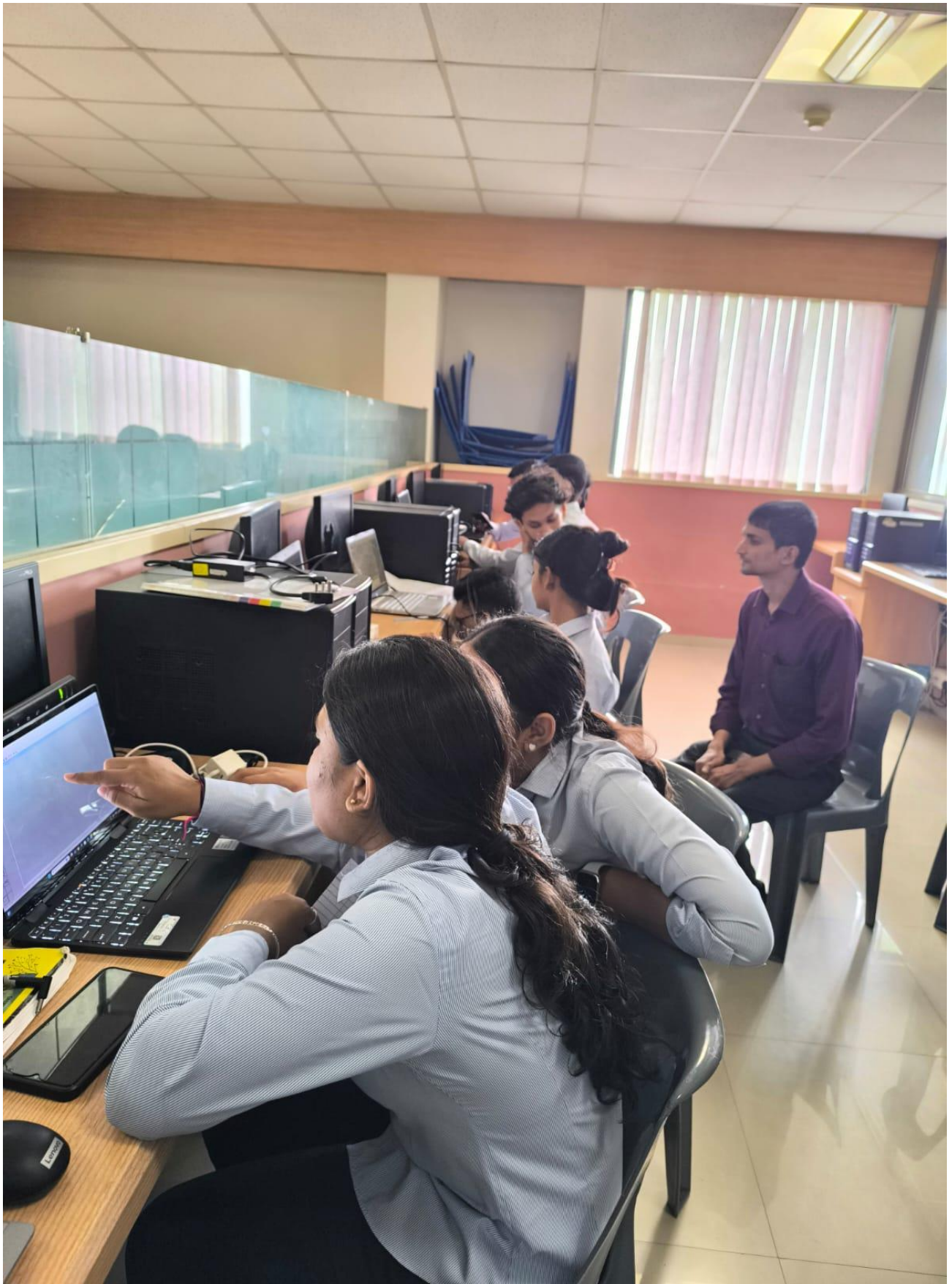


Director sir is delivering the inaugural speech



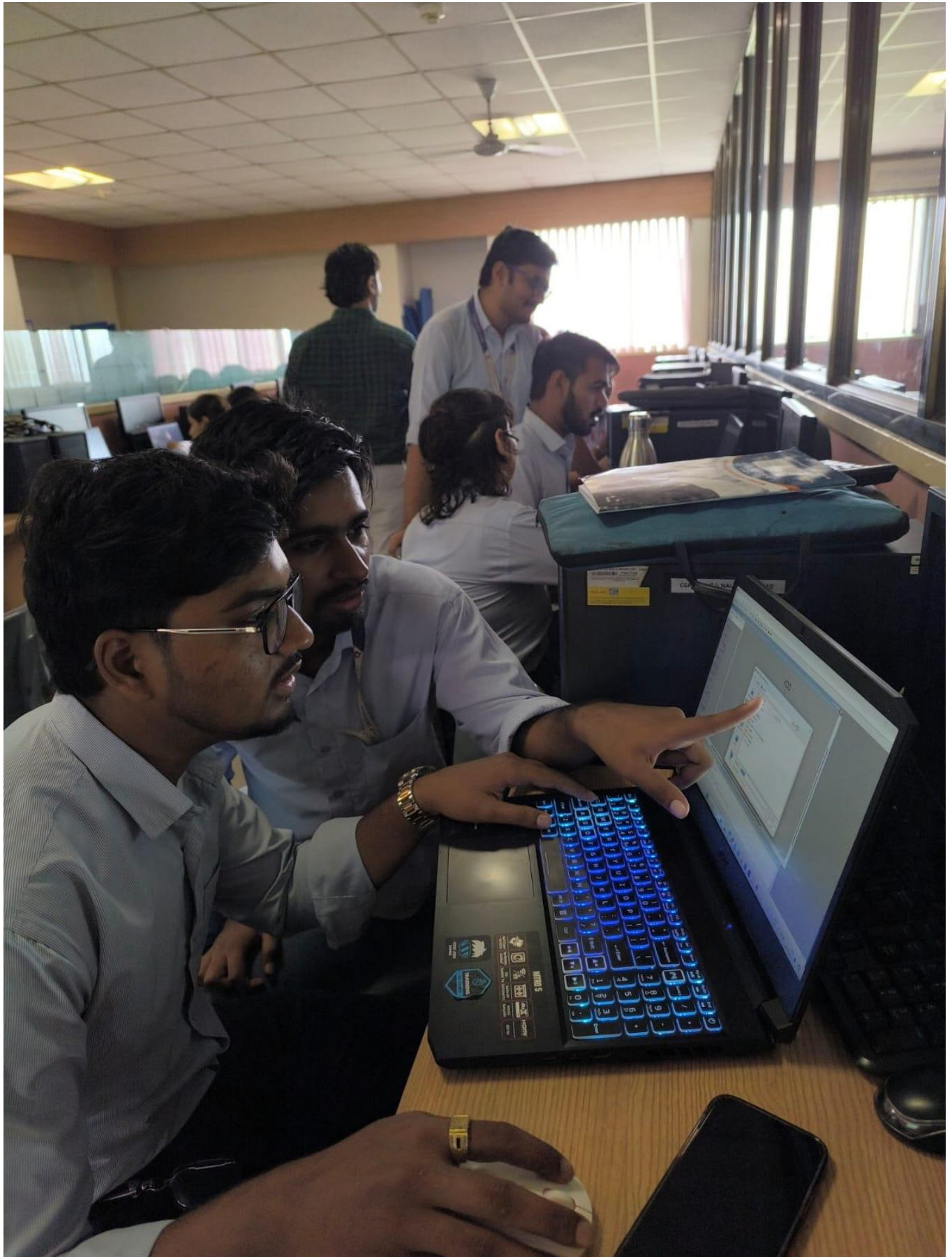


**Key note speaker – Prof. Chintan Panchal is explaining fundamentals of VLSI Design**

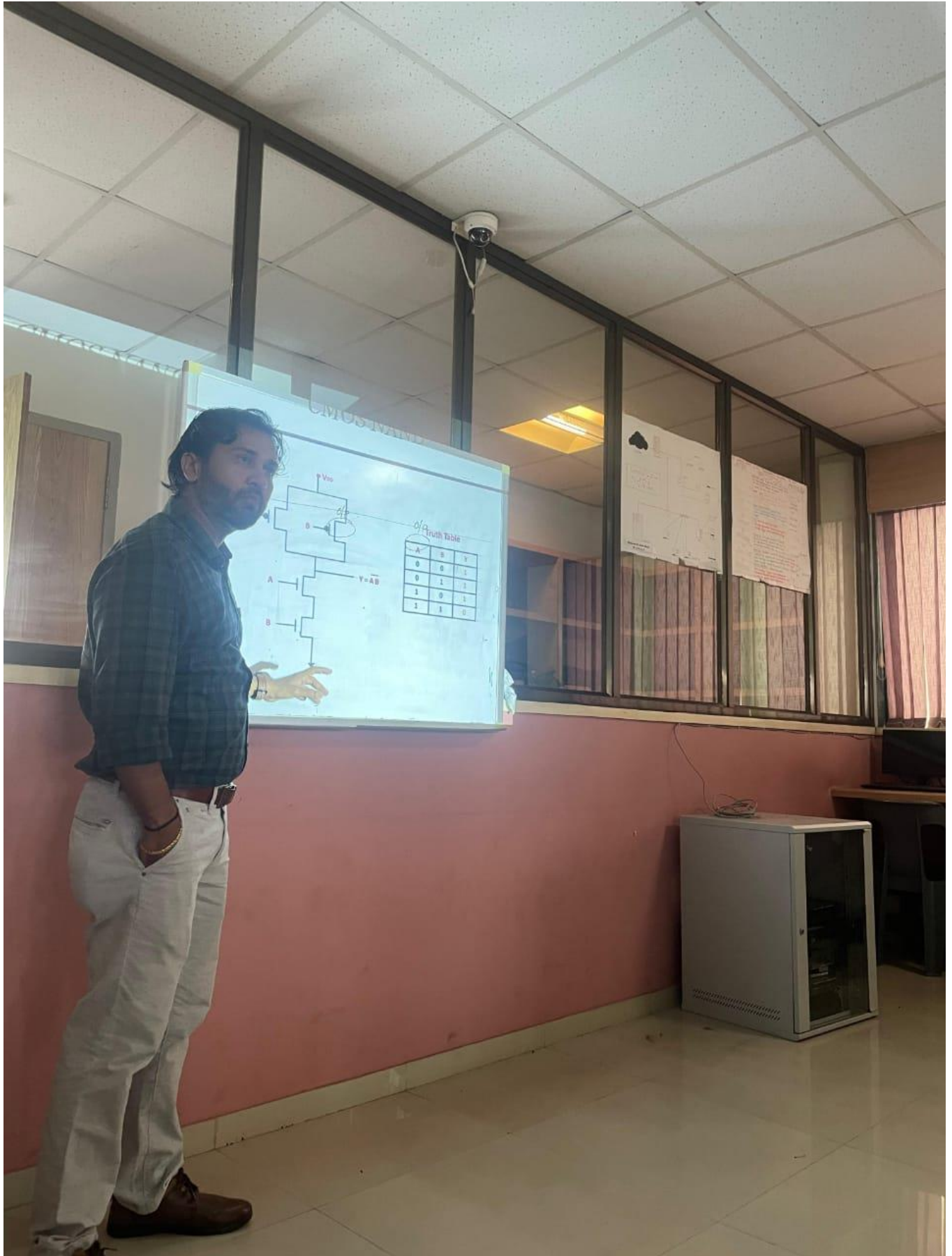


**Students doing hands on practice**





**Students doing hands on practice**



**Key note speaker Prof. Chintan Panchal is delivering technical session**





**Hands on practice and live demonstration is conducted with students, expert and faculty members**



**Felicitation of key note speaker Prof. Chintan Panchal**





**Group photo session with students, key note speaker, head of the department, coordinator and faculty members**



### **Concluding Remarks:**

- The one-day hands on workshop were successful and very informative. Students of EC and ICT engineering interacted with the key note speaker very well and discussed the trouble shooting, questions and queries raised during the hands-on session and demonstration of the VLSI schematic and lay out design procedure.

Report prepared by: Mr. Chintan Desai

Date: 26/09/2024

Sign of the HOD